

(19)



Europäisches Patentamt
European Patent Office
Office européen des brevets



(11)

EP 0 940 257 A2

(12)

EUROPEAN PATENT APPLICATION

(43) Date of publication:
08.09.1999 Bulletin 1999/36

(51) Int Cl.⁶ B41J 2/16

(21) Application number: 99301512.2

(22) Date of filing: 01.03.1999

(84) Designated Contracting States:
AT BE CH CY DE DK ES FI FR GB GR IE IT LI LU
MC NL PT SE
Designated Extension States:
AL LT LV MK RO SI

(30) Priority: 02.03.1998 US 33987

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(54) Direct imaging polymer fluid jet orifice

(57) A process for creating and an apparatus employing shaped orifices in a semiconductor substrate (20). A layer of slow cross-linking material (34) is applied on the semiconductor substrate (20). An orifice image (42) and a fluid-well image (43) is transferred to the layer

of slow cross-linking material (34). That portion of the layer of slow cross-linking material (34) where the orifice image (42) is located is then developed along with that portion of the layer of slow cross-linking material (34) where the fluid well image (43) is located to define an orifice opening in the semiconductor substrate (20).

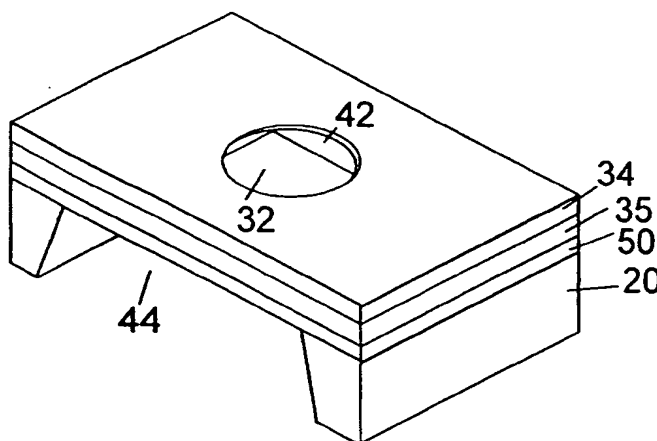


Fig. 1B

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Description

BACKGROUND OF THE INVENTION

[0001] This invention generally relates to thermal inkjet printing. More particularly, this invention relates to the apparatus and process of manufacturing precise polymer orifices comprising epoxy, polyimide or other negative acting photoresist material using direct imaging techniques.

[0002] Thermal inkjet printers typically have a printhead mounted on a carriage that traverses back and forth across the width of the paper or other medium feeding through the printer. The printhead includes an array of orifices (also called nozzles) which face the paper. Ink (or another fluid) filled channels feed the orifices with ink from a reservoir ink source. Applied individually to addressable energy dissipation elements (such as resistors), energy heats the ink within the orifices causing the ink to bubble and thus expel ink out of the orifice toward the paper. Those skilled in the art will appreciate that other methods of transferring energy to the ink or fluid exist and still fall within the spirit, scope and principle of the present invention. As the ink is expelled, the bubble collapses and more ink fills the channels from the reservoir, allowing for repetition of the ink expulsion.

[0003] Current designs of inkjet printheads have problems in their manufacturing, operating life and accuracy in directing the ink onto the paper. Printheads currently produced comprise an inkfeed slot through a substrate, a barrier interface (The barrier interface channels the ink to the resistor and defines the firing chamber volume. The barrier interface material is a thick, photosensitive material that is laminated onto the substrate, exposed, developed, and cured.), and an orifice plate (The orifice plate is the exit path of the firing chamber that was defined by the barrier interface. The orifice plate is typically electroformed with nickel (Ni) and then coated with gold (Au), palladium (Pd), or other precious metals for corrosion resistance. The thickness of the orifice plate and the orifice opening diameter are controlled to allow repeatable drop ejection when firing.). During manufacturing, aligning the orifice plate to the substrate with barrier interface material requires special precision and special adhesives to attach it. If the orifice plate is warped or if the adhesive does not correctly bond the orifice plate to the barrier interface, poor control of the ink drop trajectory results and the yield or life of the printhead is reduced. If the alignment of the printhead is incorrect or the orifice plate is dimpled (non-uniform in its planarization), the ink will be ejected away from its proper trajectory and the image quality of the printout is reduced. Because the orifice plate is a separate piece in conventionally constructed printheads, the thickness required to prevent warping or buckling during manufacturing requires the height (related to thickness of the orifice plate) of the orifice bore to be higher than necessary for thermal efficiency. Usually, a single orifice plate is attached

to a single printhead die on a semiconductor wafer that contains many printheads. It is desirable to have a process that allows for placement of the orifice plates all at once across an entire semiconductor wafer to increase productivity as well as ensure accuracy of orifice placement.

[0004] The ink within the firing chamber fills the orifice bore up to the external edges of the orifice plate. Thus, another problem with this increased height of ink in the orifice bore is that it requires more energy to eject the ink. Additionally, high quality photo printing requires higher resolutions and thus smaller drops of ink. Therefore, a need for a thinner orifice plate that is manufacturable exists. Furthermore, as the quantity of ink expelled in each drop becomes smaller, more orifices are required within the printhead to create a given pattern in a single passing of the printhead over the print medium at a fixed print speed. To prevent the printhead from overheating due to the increased number of orifices, the amount of energy used per orifice must be reduced.

[0005] Additionally, in the past, the lifetime of the printhead was adequate. The printhead was part of a disposable pen that was replaced after the ink supply ran out. However, user expectations for quality are driving the need to have a low cost, long life printhead with multiyear permanence and the present invention helps fulfill this expectation.

SUMMARY OF THE INVENTION

[0006] A process for creating and an apparatus employing shaped orifices in a semiconductor substrate is described. A first layer of material is applied on the semiconductor substrate then a second layer of material is then applied upon the first layer of material. An orifice image is then transferred to the first layer of material and a fluid-well image is transferred to the second layer of material. That portion of the second layer of material where the orifice image is located is then developed along with that portion of the first layer of material where the fluid well is located to define an orifice in the substrate.

[0007] The volume of the orifice chamber is defined by the orifice image shape and the thickness of the second layer of material. The volume of the fluid-well chamber is defined by the fluid-well image shape and the thickness of the first layer of material.

BRIEF DESCRIPTION OF THE DRAWINGS

[0008] Fig. 1A illustrates the top view of a single orifice of the preferred embodiment.

[0009] Fig. 1B is an isometric cross sectional view of the orifice illustrating the basic structure.

[0010] Figs. 2A through 2H illustrate the process steps of the preferred embodiment to create an in-situ orifice. The cut-away view is the AA perspective from Fig. 1A.

[0011] Fig. 3A is the top view of a printhead showing multiple orifices.

[0012] Fig. 3B is the bottom view of the printhead shown in Fig. 3A.

[0013] Fig. 4 shows a print cartridge that utilizes a printhead, which may employ the present invention.

[0014] Fig. 5 shows a printer mechanism using a print cartridge that has a printhead, which may employ the present invention.

[0015] Fig. 6A illustrates a mask pattern used to create an alternate embodiment of the invention.

[0016] Fig. 6B illustrates a mask pattern that is possible using the preferred embodiment of the invention.

[0017] Fig. 7A illustrates the top view of the preferred embodiment of the invention.

[0018] Fig. 7B illustrates a side view of the preferred embodiment of the invention showing the relevant dimensions used to define the reentrant orifice.

[0019] Fig. 8 is a graph representing the design trade-offs of refill time and overshoot based on the height ratio of the reentrant orifice of the preferred embodiment.

[0020] Fig. 9A through Fig. 9G illustrate the process steps to create a single layer version of the in-situ orifice.

[0021] Fig. 10A through Fig. 10E illustrate results in the process to create a multi-density level mask used in the preferred embodiment of the invention.

DETAILED DESCRIPTION OF THE PREFERRED AND ALTERNATE EMBODIMENTS

[0022] The invention relates to a novel polymer orifice fabrication process that creates a multi-material sandwich of photoimagable layers over the substrate and that does not require a Ni orifice plate or barrier interface material. Each photoimagable layer has different rate of cross-linking for a given intensity of energy. Additionally, the invention encompasses a design topology using the photoimagable layers that produces a top-hat shaped reentrant (directed inwards) profile orifice. The top-hat orifice can be tailored by varying process parameters to optimize drop ejection characteristics. This top-hat design topology offers several advantages over straight walled or linear tapered architectures. The top-hat shaped reentrant orifice chamber, which ejects the fluid drops, is easily defined by a fluid-well chamber and an orifice chamber. The area and shape of each chamber, as viewed looking into the orifice, is defined by using a patterned mask or set of masks. The masks allow for controlling the entrance diameter, exit diameter and firing chamber volume based on the orifice layer thickness or height. The height of the orifice chamber and the height of the fluid-well chamber are independently controlled to allow for optimum process stability and design latitude. By controlling the shape, area and height of the orifice and fluid-well chambers, the designer can control the drop size, drop shape, and dampen the effect of the blowback (that part of the bubble which expels the ink that expands opposite to the direction of drop ejection)

and to some extent the refill speed (the time required to have ink fill the entire top-hat orifice structure). In addition, this top-hat topology allows the fluid feed slots, which deliver fluid to the orifice, to be placed further away from the energy dissipation element used to eject the fluid to reduce the possibility of the bubble entering the fluid supply path and thus creating a blockage.

[0023] The direct imaging polymer orifice normally comprises two or more layers of negative acting photoresist materials with slightly different dissolution rates. The dissolution rates are based on the different materials of each layer having a different molecular weight, physical composition, or optical density. In an exemplary process using two layers, a "slow" photoresist that requires 500mJoules/cm² intensity of electromagnetic energy for cross-linking is applied on a substrate. In an fluid-jet printhead this substrate is comprised of a semiconductor material that has had a stack of thin-film layers applied to its surface. A "fast" photoresist that requires just 100 mJoules/cm² intensity of electromagnetic energy for cross-linking is applied on the layer of slow photoresist. After curing, the substrate photoresist layers are exposed through a mask at a very high intensity of at least 500 mJoules/cm² to define the fluid-well chamber. The intensity is high enough to cross-link both the top and lower layers. The substrate photoresist layers are then exposed through another mask with low intensity electromagnetic energy of 100 mJoules/cm² to define the orifice chamber. It is important that the intensity of the second exposure be low enough so the lower orifice layer of slow photoresist that is beneath the orifice opening is not cross-linked.

[0024] Polymer material is well known in the IC industry for its ability to planarize over thin-film topographies. Empirical data shows that orifice plate topography variation can be kept well within 1 micron. This feature is important to provide a consistent drop trajectory.

[0025] In addition, many different polymer materials having negative acting photoresist properties exist. Exemplary polymer materials are polyimide, epoxy, polybenzoxazoles, benzocyclobutene, and sol gels. Those skilled in the art will appreciate that other negative acting photoresist polymer materials exist and still fall within the spirit and scope of the invention. By adding optical dye (such as Orange #3, ~2% weight) to transparent polymer material, a slow photoresist can be made from fast photoresist that has no dye or a small amount of dye. Another embodiment would be to coat a layer of polymer material with a thin layer of dye. Alternative methods to create slow photoresist comprise mixing polymers with different molecular weights, with different wavelength absorption characteristics, with different developing rates, and using pigments. Those skilled in the art will appreciate that other methods to slow the photosensitivity of polymers exist and still fall within the spirit and scope of the invention.

[0026] Fig. 1A illustrates the top view of a single orifice (also called a nozzle or a hole) using the preferred

embodiment of the present invention. Top orifice layer 34 is comprised of fast cross-linking polymer such as photoimagable epoxy (such as SU8 developed by IBM) or photoimagable polymer (such as OCG, commonly known in the art). The top orifice layer 34 is used to define the shape and height of the orifice 42 opening. Hidden within the orifice layer are fluid feed slots 30 and a fluid-well 43. Fluid, such as ink, flows into the fluid-well 43 through the fluid feed slots 30 and is heated by energy dissipation element 32 forming a fluid vapor bubble that forcibly ejects the remaining fluid from the orifice 42. View AA shows the direction of observation for the cross-sectional views in later figures.

[0027] Fig. 1B is an isometric cross-sectional view of the single orifice shown in Fig. 1A of a fully integrated thermal (FIT) fluid jet printhead. Lower orifice layer 35 is applied on top of a stack of thin-film layers 50, which have been processed by individual layers and incorporated onto the surface of a semiconductor substrate 20. An exemplary orifice would have an orifice 42 diameter of 16 μm , a fluid-well 43 length of 42 μm , a fluid-well 43 width of 20 μm , a top orifice layer 34 thickness of 6 μm , and a lower orifice layer 35 thickness of 6 μm . Semiconductor substrate 20 is etched after the stack of thin-film layers 50 have been applied to provide fluid feed channel 44, which supplies fluid to the fluid feed slots 30 (not shown). Fluid feed slots 30 are defined within the stack of thin-film layers 50.

[0028] Figs. 2A through 2H illustrate the various process steps used to create alternative embodiments of the invention. Fig. 2A illustrates semiconductor substrate 20 after it has been processed to incorporate the stack of thin-film layers 50, which includes energy dissipation element 32. The stack of thin-film layers 50 has been processed such that fluid feed slots 30 extend through its entire thickness.

[0029] Fig. 2B illustrates the semiconductor substrate 20 after the lower orifice layer 35, comprised of a slow cross-linking polymer, is applied on top of the stack of thin-film layers 50. The slow cross-linking polymer is applied using a conventional spin-coating tool such as those manufactured by Karl Suss KG. The spin-coating process associated with the spin-coating tool allows for a planar surface to be formed as the slow cross-linking polymer 35 fills the fluid feed slots 30 and the surface of stack of thin-film layers 50. An exemplary process for spin coating is to spread a layer of resist on a semiconductor wafer with the spin coating tool set to 70 rpm with an acceleration of 100 rpm/s and a spread time of 20 secs. The wafer is then stopped from spinning with a deceleration of 100 rpm/s and rests for 10 secs. The wafer is then spun at 1060 rpm at an acceleration rate of 300 rpm/s for 30 secs to spread the resist over the entire wafer. Alternative polymer application processes include roll-coating, curtain coating, extrusion coating, spray coating, and dip-coating. Those skilled in the art will appreciate that other methods to apply the polymer layers to the substrate exist and still fall within the spirit

and scope of the invention. The slow cross-linking polymer is made by mixing optical dye (such as orange #3, ~2% weight) into either a photoimagable polyimide or photoimagable epoxy transparent polymer material. By adding the dye, the amount of electromagnetic energy required is greater than non-dye mixed material to cross-link the material.

[0030] Fig. 2C illustrates the result of applying the top orifice layer 34 comprised of a fast cross-linking polymer on lower orifice layer 35.

[0031] Fig. 2D illustrates a strong intensity of electromagnetic radiation 11 being applied to top orifice layer 34 and lower orifice layer 35. The energy supplied by the electromagnetic radiation must be sufficient to cross-link both the top orifice layer 34 and lower orifice layer 35 where exposed (shown in Figs. 2D, 2E and 2F as X-out areas). In an exemplary embodiment, this step is done using a SVG Micralign tool set at 300 mJoules with a focus offset of +9 μm . This step defines the shape and area of the fluid-well 43 in the orifice.

[0032] Fig. 2E illustrates the next step of the process in which a lower intensity of electromagnetic energy 12 is applied to the top orifice layer 34 and lower orifice layer 35. The total energy expended during this step (either by limiting the intensity or time of exposure or a combination of both) is only sufficient to cross-link the fast cross-linking polymer in top orifice layer 34. In an exemplary embodiment, this step is done using a SVG Micralign tool set at 60.3 mJoules with a focus offset of +3 μm . This step defines the shape and area of orifice opening 42.

[0033] Fig. 2F illustrates the preferred embodiment exposure process. Instead of using two masks, one to define the fluid-well as in Fig. 2D and one to define an orifice opening 42 as in Fig. 2E, only one mask is used. This approach reduces the possible alignment mistakes when using two separate masks. This mask is comprised of three separate density regions per orifice opening (see Figs. 6A and 6B) forming a multi-density level mask. One region is essentially non-opaque to the electromagnetic energy. The second region is partially opaque to the electromagnetic energy. The third region is completely opaque to the electromagnetic energy.

[0034] The first region allows a strong intensity of electromagnetic energy 11 to pass through the mask to fully cross-link and define the orifice layers where no photoimagable material is to be removed. Both top orifice layer 34 and lower orifice layer 35 are cross-linked to prevent removal during developing. The second region is designed to allow only a lower intensity of electromagnetic energy 12 through to cross-link the top orifice layer 34 while leaving the material beneath the second region in lower orifice 35 uncross-linked. The third region (fully opaque) is used to define the shape and area of the orifice opening 42. Because no electromagnetic energy is allowed through this third region, the cross-linking polymer beneath the opaque third region of the mask will not be exposed thus will be removed

when developed later.

[0035] Fig. 2G illustrates the developing process step where material in the top orifice layer 34 and lower orifice 35, including the material in fluid-feed slots 30, is removed. An exemplary process is to use a 7110 Solitec developer tool with a 70 sec. development in NMP @ 1 krpm. and 8 sec mix of IPA & NMP @ 1 krpm. a 10 sec. rinse with IPA @ 1 krpm, and a 60 second spin @ 2 krpm.

[0036] Fig. 2H illustrates the result after a tetramethyl ammonium hydroxide (TMAH) backside etch process (see U. Schnakenburg, W. Benecke and P. Lange, TMAHW Etchants for Silicon Micromachining, Tech. Dig. 6th Int. Conf. Solid State Sensors and Actuators (Transducers '91). San Francisco, CA, USA, June 24-28, 1991 pp. 815-818) is performed to create fluid feed channel 44 which opens into fluid feed slots 30 to allow fluid to enter fluid-well chamber 43 and ultimately ejected out of orifice opening 42

[0037] Fig. 3A represents an exemplary printhead 60 which comprises a plurality of orifice opening 42 found in top orifice layer 34 and lower orifice layer 35. The orifice layers are applied on a stack of thin-film layers 50, which has been processed on semiconductor substrate 20.

[0038] Fig. 3B illustrates the opposite side of printhead 60 to reveal fluid feed channels 44 and fluid feed slots 30.

[0039] Fig. 4 illustrates an exemplary embodiment of a print cartridge 100, which uses printhead 60. Such a print cartridge could be similar to HP51626A available from Hewlett-Packard Co. Printhead 60 is bonded onto a flex-circuit 106 that couples control signals from electrical contacts 102 to the printhead 60. Fluid is held in the fluid reservoir 104, which comprises a fluid delivery assemblage of which an exemplary type, a sponge 108 and standpipe (not shown), is exhibited. The fluid is stored in sponge 108 and delivered to printhead 60 through the standpipe.

[0040] Fig. 5 illustrates an exemplary liquid jet recording apparatus 200, similar to a Hewlett-Packard Deskjet 340 (C 2655A) using the print cartridge 100 of Fig. 4. Medium 230 (such as paper) is taken from the medium tray 210 and conveyed along its length across the print cartridge 100 by the medium feed mechanism 260. The print cartridge 100 is conveyed along the width of the medium 230 on a carriage assemblage 240. Medium feed mechanism 260 and carriage assembly 240 together form a conveyance assemblage for transporting the medium 230. When the medium 230 has been recorded onto, it is ejected on medium output tray 220.

[0041] Fig. 6A illustrate a single multi-density level mask 140; this is used to form the orifice opening 42 in an alternative embodiment of the present invention. The opaque area 142 is used to define the shape and area of the orifice opening 42. Partially opaque area 144 is used to define the shape and area of the fluid-well. Non-opaque area 146 is essentially transparent to the elec-

tromagnetic energy and this area of the mask defines those areas of the top orifice layer 34 and lower orifice layer 35 which will be cross-linked and not removed when developed. The shape of opaque area 142 matches the geometric shape of partially opaque area 144 in order to optimize the developing process.

[0042] Fig. 6B illustrates the preferred embodiment of the single multi-density level mask 150 in which the geometric shape of the opaque area 152 is different from the geometric shape of the partially opaque area 154. This technique is allowed due to the direct imaging method that allows for separate definition of the fluid-well shape and orifice opening shape. This technique allows for optimal design of the fluid-well to allow for fast refill rates, bubble blow back percentage and maximum density of multiple orifices on a printhead. When a fluid drop is ejected from an orifice, the drop has a main body shape and a trailing tail, which combined form the drop volume. The direct imaging method allows for the optimal design of orifice opening 42 to provide the proper volume of fluid ejected, the tail design of the ejected fluid and shape of the fluid as it exits the orifice, which allows for minimizing breakup of the fluid on its flight path to the medium. Non-opaque area 156 is essentially transparent to the electromagnetic energy and this area of the mask defines those areas of the top orifice layer 34 and lower orifice layer 35 which will be cross-linked and not removed when developed. In this embodiment, an exemplary mask would have a transmissivity for non-opaque area 156 of essentially 100%, a transmissivity for partially opaque area 154 is essentially 20%, and the transmissivity for opaque area 152 is essentially 0%.

[0043] The ability to have different shapes allows for the fluid feed slots 30 to be placed further away from the energy dissipation element 32 to reduce the possibility of gulping the blowback of the bubble thus limiting air injection in through the orifice.

[0044] Furthermore, due to the ability to control the thickness of both the lower orifice layer 35 and the upper orifice layer 34 with the ability to control the individual shapes of the fluid-well and orifice opening, a general design for an orifice architecture can be accomplished.

[0045] Fig. 7A illustrates the top view of the preferred orifice architecture. Orifice opening 174 is a circular shape and fluid-well 172 is of a rectangular shape. Fig. 7B illustrates the side view of the orifice as seen through the BB perspective of Fig. 7A. The top orifice layer 168 has a top orifice height 162, which along with the area of orifice opening 174 determines the volume of orifice chamber 176. The lower orifice layer 170 has a lower orifice height 164, which along with the area of fluid-well 172 determine the volume of the fluid-well chamber 180. The total orifice height 166 is the sum of both top orifice height 162 and lower orifice height 164. The ratio of the lower orifice height 164 to the upper orifice height 162 defines a critical parameter, the height ratio, where:

$height_ratio = lower_orifice_height/top_orifice_height.$

This height ratio controls both the overshoot volume of the ejected drop, related to the length of its trailing tail, and the refill time, the time required for refilling the orifice with fluid after fluid ejection.

[0046] Fig. 8 is a graph that illustrates the effect of the height ratio vs. the refill time and the height ratio vs. the overshoot volume for an exemplary orifice diameter of 16 μm and a fluid-well length of 42 μm and width of 20 μm . Using this graph would allow the designer of a print-head to choose the layer thickness for a desired ejected drop shape.

[0047] Figs. 9A to 9E illustrate the steps of an alternate embodiment of the invention which uses a single layer of slow cross-linking polymer and employs an underexposure and an overexposure of electromagnetic energy to the slow cross-linking polymer material as a method to form the separate layers.

[0048] Fig. 9A illustrates a processed semiconductor substrate 20 which has a stack of thin-film layers 50 applied on it, which contain energy dissipation element 32 and fluid feed slots 30.

[0049] Fig. 9B illustrates the application of a layer of slow cross-linked material 34 on the stack of thin-film layers 50 and fills in fluid feed slots 30.

[0050] Fig. 9C illustrates the exposure of the layer of slow cross-linking polymer 34 with a low dosage of electromagnetic energy 12 to define the orifice opening. The exposure dosage is just enough to underexpose and cross-link the slow cross-linking polymer to a desired depth. An exemplary exposure would be 60.3 mJoules.

[0051] Fig. 9D illustrates the exposure of the layer of slow cross-linking polymer 34 with a high dosage sufficient to overexpose and cross-link all of the layer of slow cross-linking polymer 34 with a high dosage sufficient to cross-link all of the layer of slow cross-linking polymer 34 except where the fluid-well chamber is to exist. An exemplary exposure would be 300 mJoules.

[0052] Fig. 9E illustrates an alternate process step to that used in Figs. 9C and 9D using a single mask having multi-density levels to allow different dosages of electromagnetic energy to be exposed to the layer of slow cross-linking polymer 34. This technique provides for precision alignment of the orifice opening 42 and fluid-well chamber 43 while also reducing the number of process steps.

[0053] Fig. 9F illustrates the developing process in which the non cross-linked material is removed from the fluid-well chamber and orifice chamber. The orifice chamber has a slight reentrant taper due to less cross-linking of material in the depth of layer of slow cross-linking polymer 34 since the dye or other material mixed within attenuates the electromagnetic energy as it penetrates.

[0054] Fig. 9G illustrates the finished result after the backside TMAH etch process to create fluid feed chan-

nel 44 which opens into fluid feed slots 30.

[0055] Fig. 10A to Fig. 10E illustrates results of the process steps used to produce the multi-density level mask used in the single mask fabrication processes to make the holes in the orifice layer.

[0056] Fig. 10A illustrates a quartz substrate 200 that is transparent to the electromagnetic energy used to expose the photoimagable polymer used to create the orifice layers. The quartz substrate 200 must be of a suitable optical quality.

[0057] Fig. 10B illustrates quartz substrate 200 with a layer of semi-transparent dielectric material 210 applied on it. Such an exemplary material is ferrous oxide (FeO_2). On the layer of semi-transparent dielectric material 210 is applied a layer of opaque material 220, an exemplary material being chromium. Both FeO_2 and chromium can be deposited using a conventional e-beam evaporator. A layer of negative acting photo-resist is applied on the layer of opaque material 220, exposed to electromagnetic energy and developed to leave a photoresist area 230 which defines the shape and size of the fluid-well chamber.

[0058] Fig. 10C illustrates the result after the quartz substrate 200 has been conventionally etched. When the opaque material 220 is comprised of chromium, then an exemplary etch process is a standard KTI chromium etch bath. The quartz substrate 200 is then subjected to another conventional etch process to remove the semi-transparent dielectric material 210 forming semi-transparent layer 212. When FeO_2 is used for the semi-transparent dielectric material 210 an exemplary etch process is a plasman etch using an SF_6 or CF_4 plasma. The remaining photoresist 230 is then stripped.

[0059] In Fig. 10D another layer of photoresist is then applied to the quartz substrate 200, exposed to define the orifice opening shape and area then developed to create orifice pattern 240.

[0060] Fig. 10E illustrates the result after the quartz substrate 200 is processed in an etch to remove the opaque layer 222 where the orifice pattern 240 is not located thereby creating the opaque layer orifice opening pattern 224. For an opaque material that is chromium, an exemplary etch process is a wet chemical etch so that semi-transparent dielectric layer 212 is not attacked in the etch process.

[0061] The direct imaging polymer orifice process is simple, inexpensive, uses existing equipment and is compatible with current thermal fluid jet technology. It provides design flexibility and tight orifice dimension control in allowing for independent control of the orifice and fluid-well geometry. A multi-density level mask design allows for using a single exposure to provide inherent alignment of the orifice and fluid-well to improve yields and consistency.

[0062] While different reentrant orifice shapes have been shown, other reentrant shapes are possible using the aforementioned techniques and fall within the spirit and scope of the invention.

[0063] The invention addresses the need of tighter fluid jet directional control and smaller drop volume for finer resolution required for vibrant clear photographic printing. In addition, the invention simplifies manufacturing of the printhead, which lowers the cost of production, enables high volume run rates and increases the quality, reliability and consistency of the printheads. The preferred embodiment, and its alternative embodiments of the invention, demonstrate that unique orifice shapes can be created to address additional concerns or to take advantage of different properties of the fluid expelled from the printhead.

Claims

1. A method for constructing a fluid jet print head having a semiconductor substrate (20) having a first surface and a second surface having a plurality of fluid feed slots (30) extending through said semiconductor substrate (20) and coupled to a plurality of fluid feed channels (44) on said second surface, comprising the steps of:
 - applying a layer of slow cross-linking material (34) on said first surface of said semiconductor substrate (20);
 - transferring an orifice image (42) and a fluid-well image (43) to said applied layer of slow cross-linking material (34); and
 - developing those portions of said layer of slow cross-linking material (34) where said transferred orifice image (42) is located to locate a respective orifice opening and said transferred fluid-well image (43) is located to locate a respective fluid-well opening.
2. The method in accordance with claim 1, wherein said step of applying said slow cross-linking material (34) further comprises the step of selecting said slow cross-linking material (34) from a group consisting of distinct layers of photoimagable polymer and optical dyes, mixtures of photoimagable polymer and optical dyes, and photoimagable polymer.
3. The method in accordance with claim 1, wherein said step of applying said slow cross-linking material (34) further comprises the step of selecting said slow cross-linking material (34) from a group consisting of distinct layers of photoimagable epoxy and optical dyes, mixtures of photoimagable epoxy and optical dyes, and photoimagable epoxy.
4. The method in accordance with claim 1, wherein said steps of applying said layer of slow cross-linking material (34) further comprises the step of applying an 8 to 34 micron thickness of said applied layer of slow cross-linking material (34).
5. The method in accordance with claim 1, wherein said step of transferring said orifice image (42) and said fluid-well image (43) further comprises exposing said slow cross-linking material (34) with electromagnetic energy through a multi-density level mask.
6. The method in accordance with claim 1, wherein said step of transferring said orifice image (42) and said fluid-well image (43) further comprises:
 - exposing said slow cross-linking material (34) to a patterned high dosage of patterned electromagnetic energy; and
 - exposing said slow cross-linking material (34) to a patterned low dosage of patterned electromagnetic energy.
7. A printhead for ejecting fluid using a semiconductor substrate, comprising:
 - a semiconductor substrate (20) having a first surface and a second surface;
 - a stack of thin-film layers (50) affixed to said first surface of said semiconductor substrate (20), said stack of thin-film layers (50) further comprising an energy dissipating element (32), and said stack of thin-film layers (50) defining a fluid feed slot (30);
 - a layer of slow cross-linking material (34) having an orifice (42) defined therein, said slow cross-linking material (34) applied on said stack of thin-film layers (50), said orifice (42) positioned over said energy dissipating element (32) and said layer of slow cross-linking material (34) having a fluid-well (43) defined therein, said fluid-well (43) positioned over said fluid feed slot (30); and
 - a fluid feed channel (44) defined within said second surface of said semiconductor substrate (20) and opening into said fluid feed slot (30).
8. A multi-density level mask, comprising:
 - a transparent quartz substrate (200);
 - a layer of patterned semi-transparent dielectric material (212) applied on said transparent quartz substrate (200); and
 - a layer of patterned opaque material (224) applied on said layer of patterned semi-transparent dielectric material (212).
9. The multi-density level mask as in claim 8 wherein said layer of patterned semi-transparent dielectric material (212) is semi-transparent through the optical wavelength range of 365 to 436 nanometers.

10. The multi-density level mask as in claim 8 wherein said layer of patterned semi-transparent dielectric material (212) is FeO_2 .

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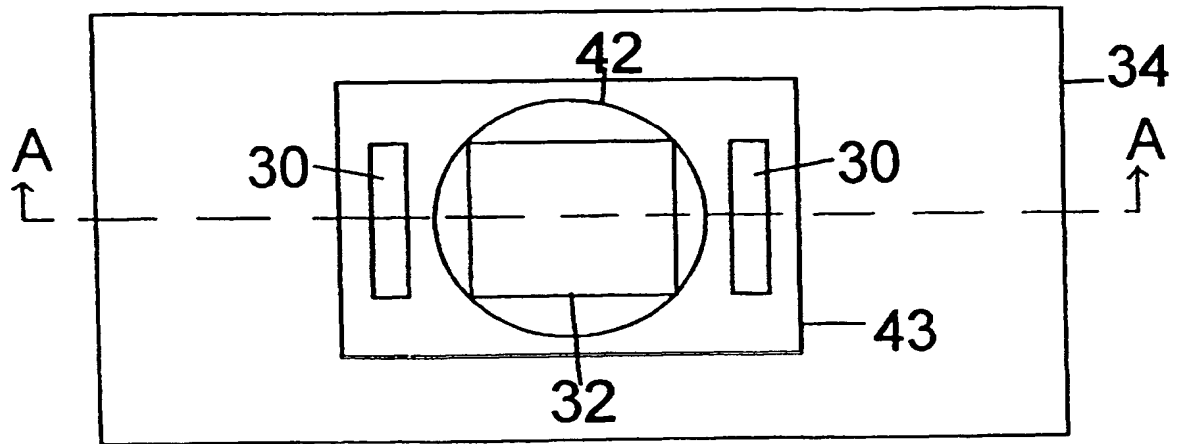


FIG. 1A

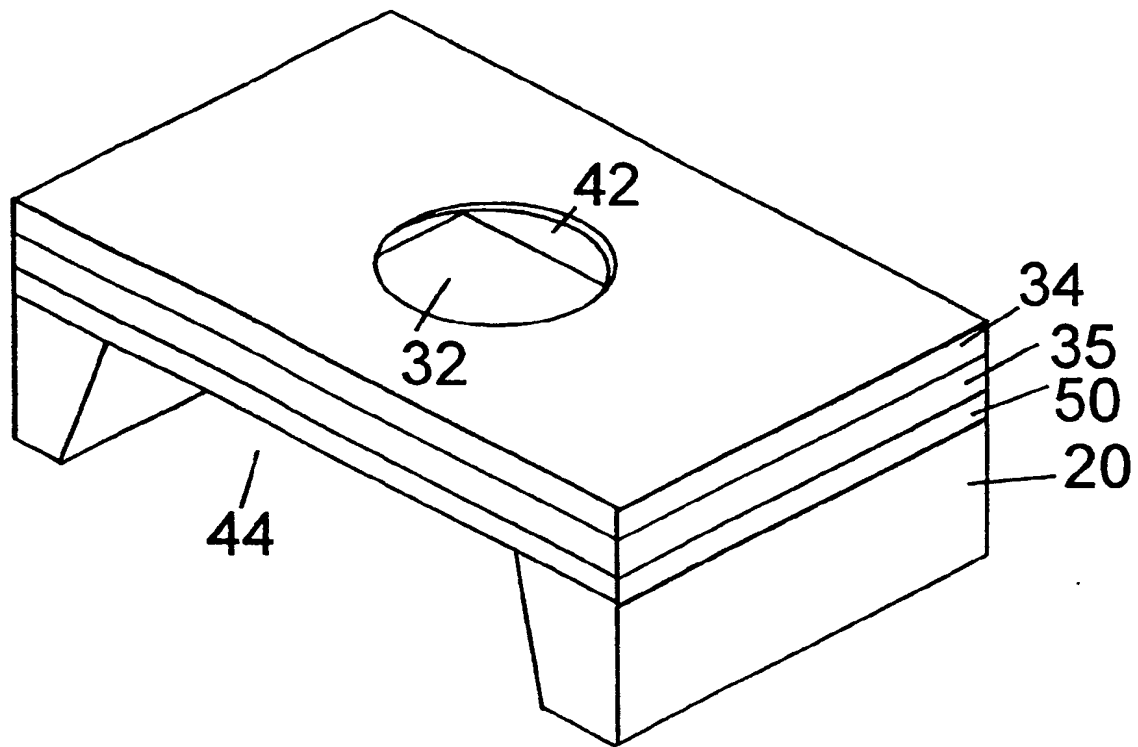


Fig. 1B

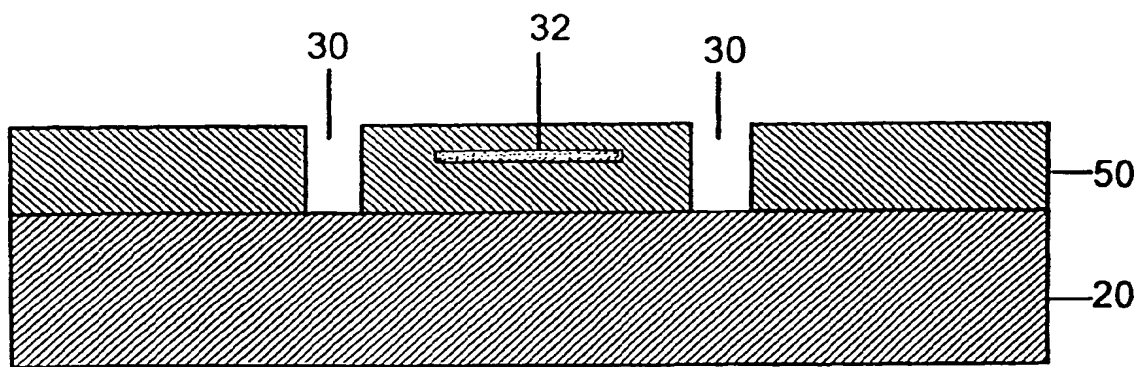


FIG. 2A

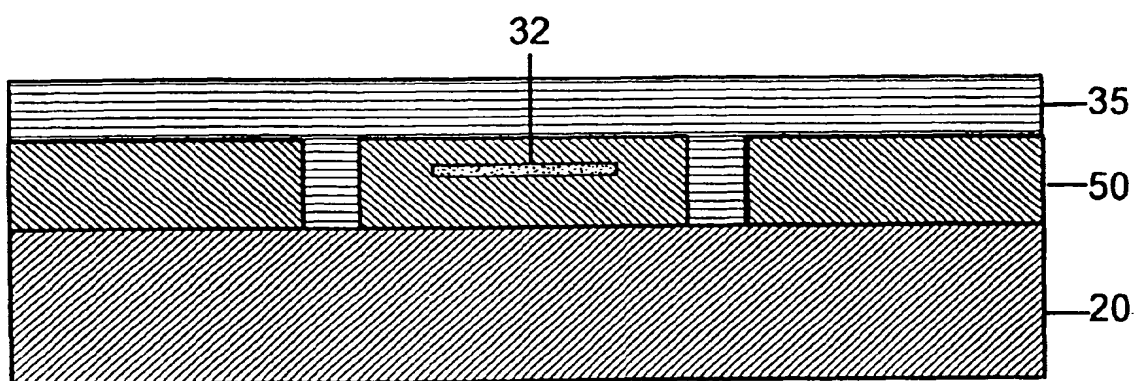


FIG. 2B

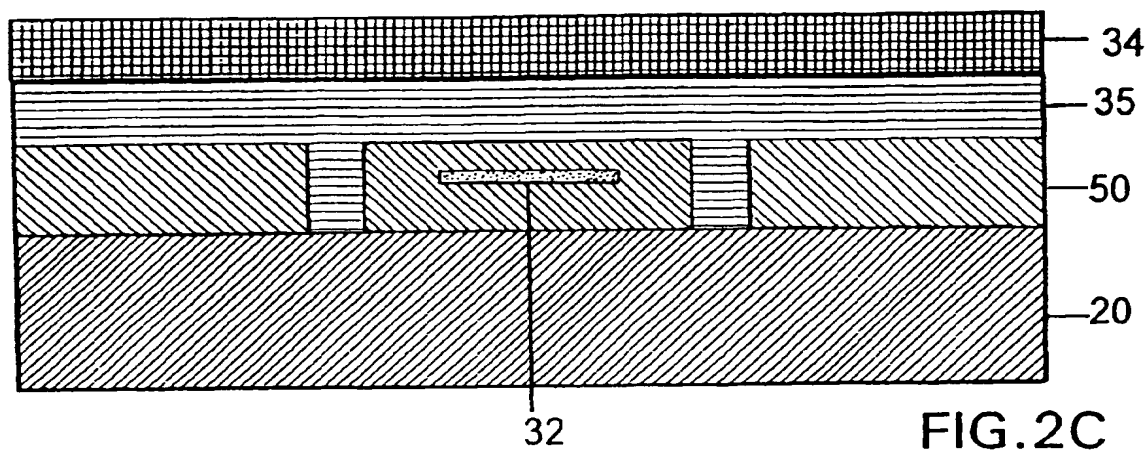


FIG. 2C

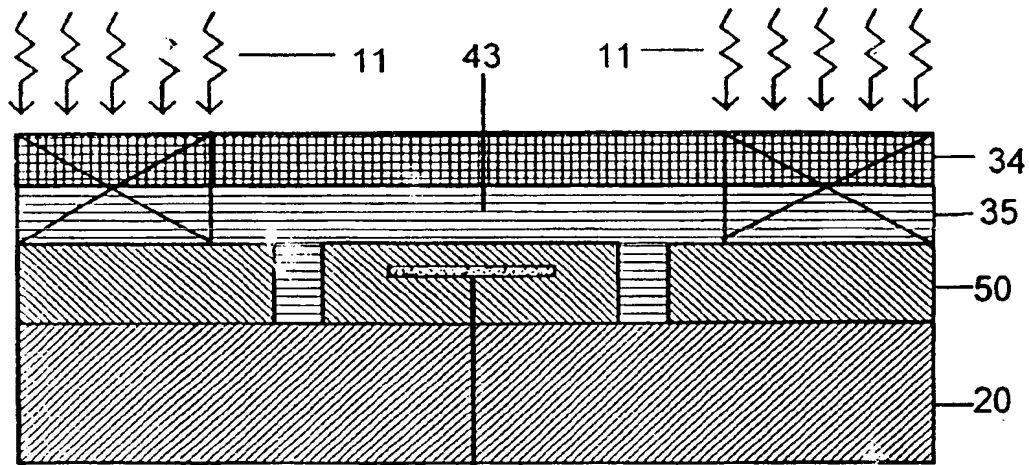


FIG. 2D

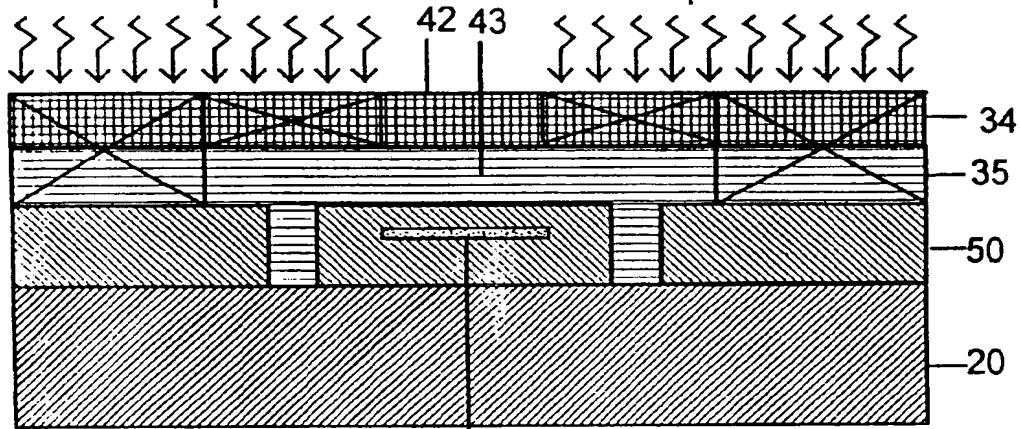


FIG. 2E

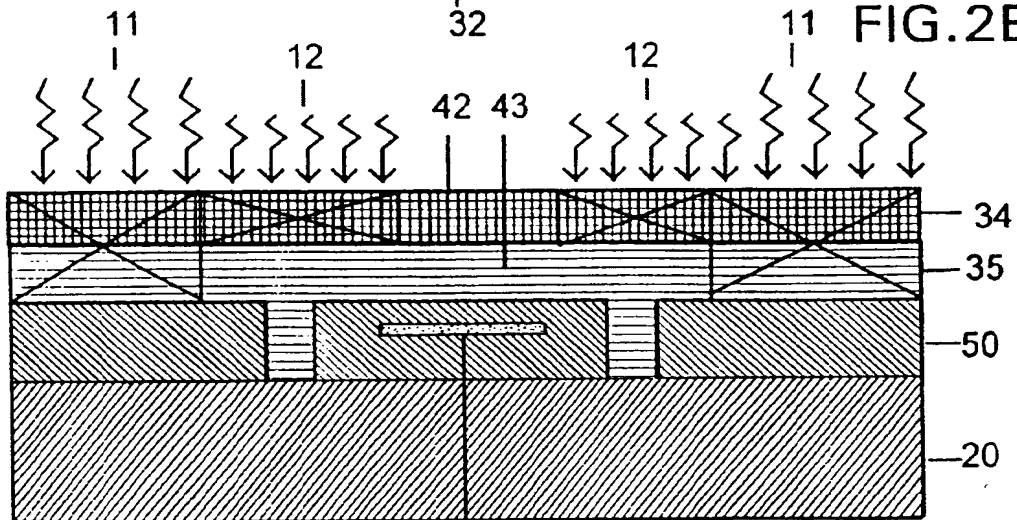
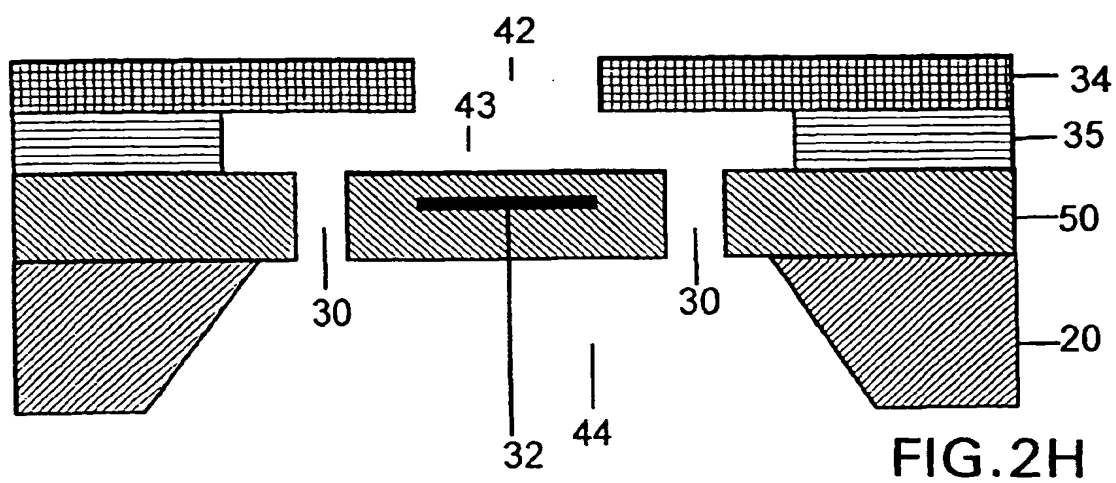
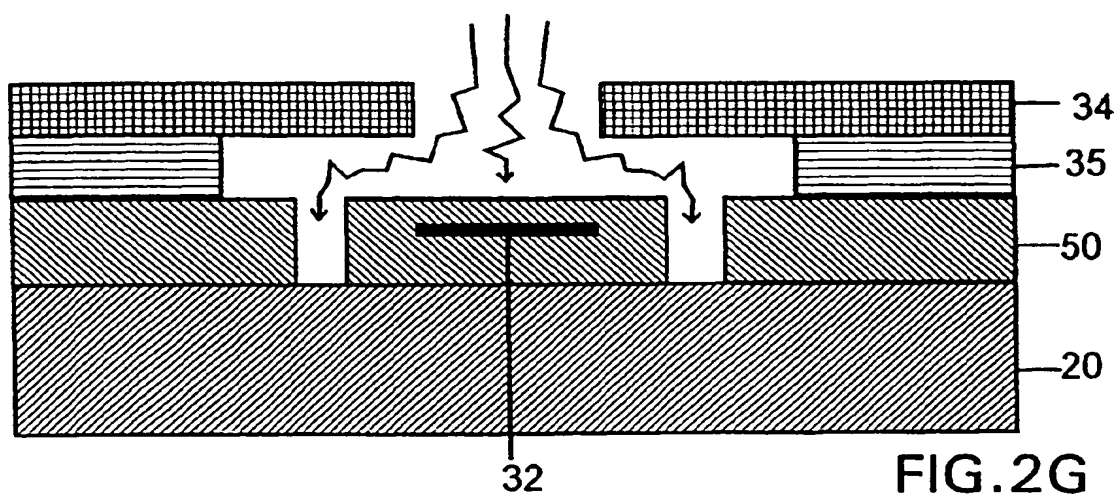


FIG. 2F



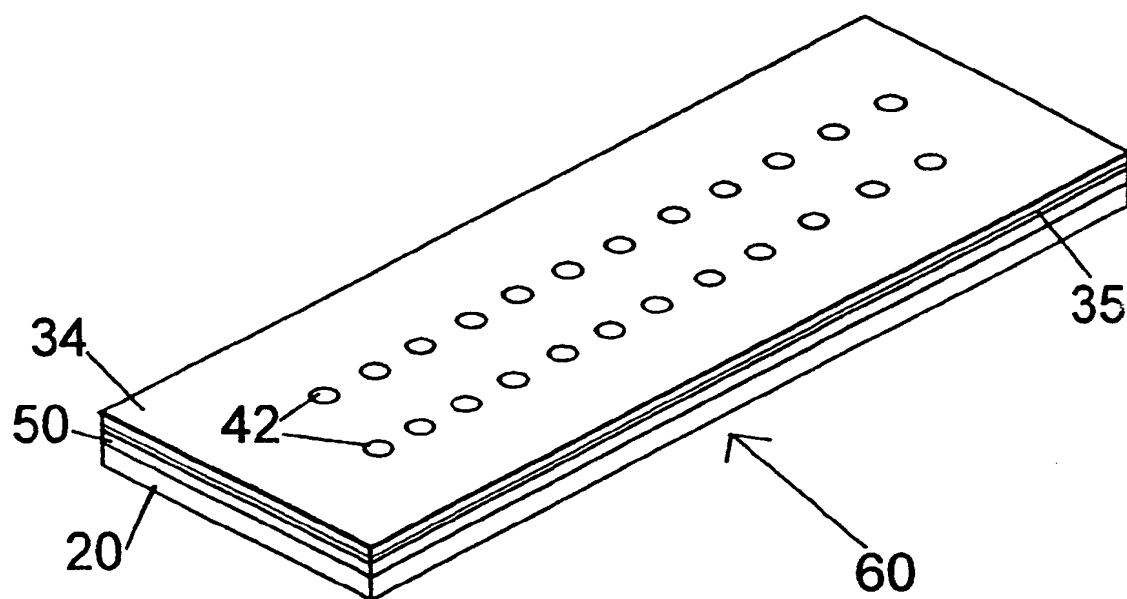


FIG. 3A

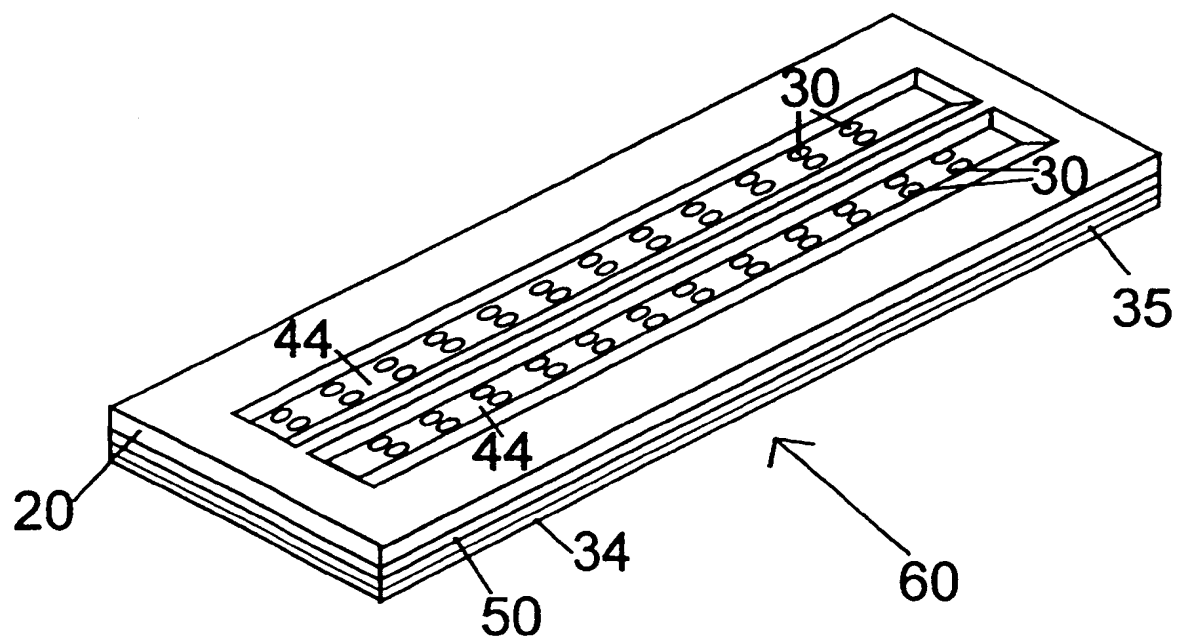


FIG. 3B

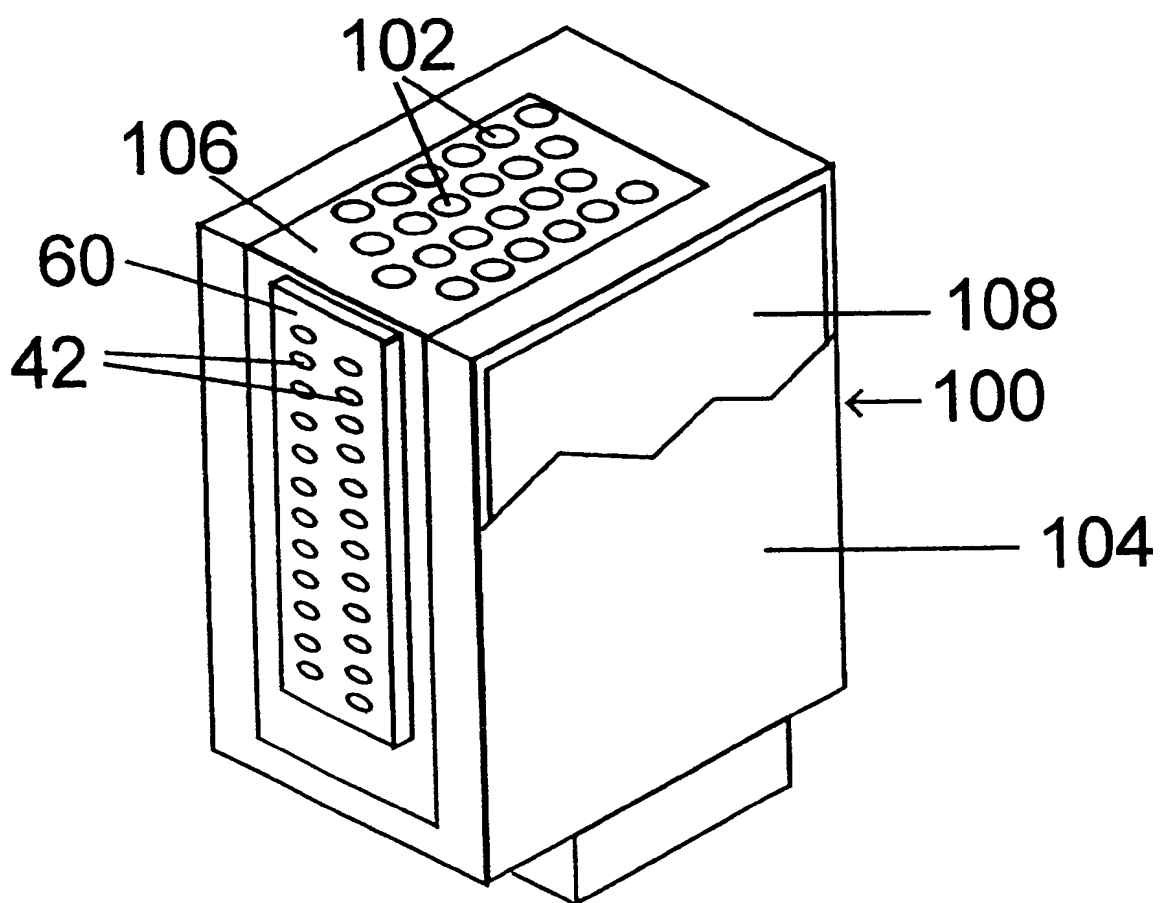


FIG. 4

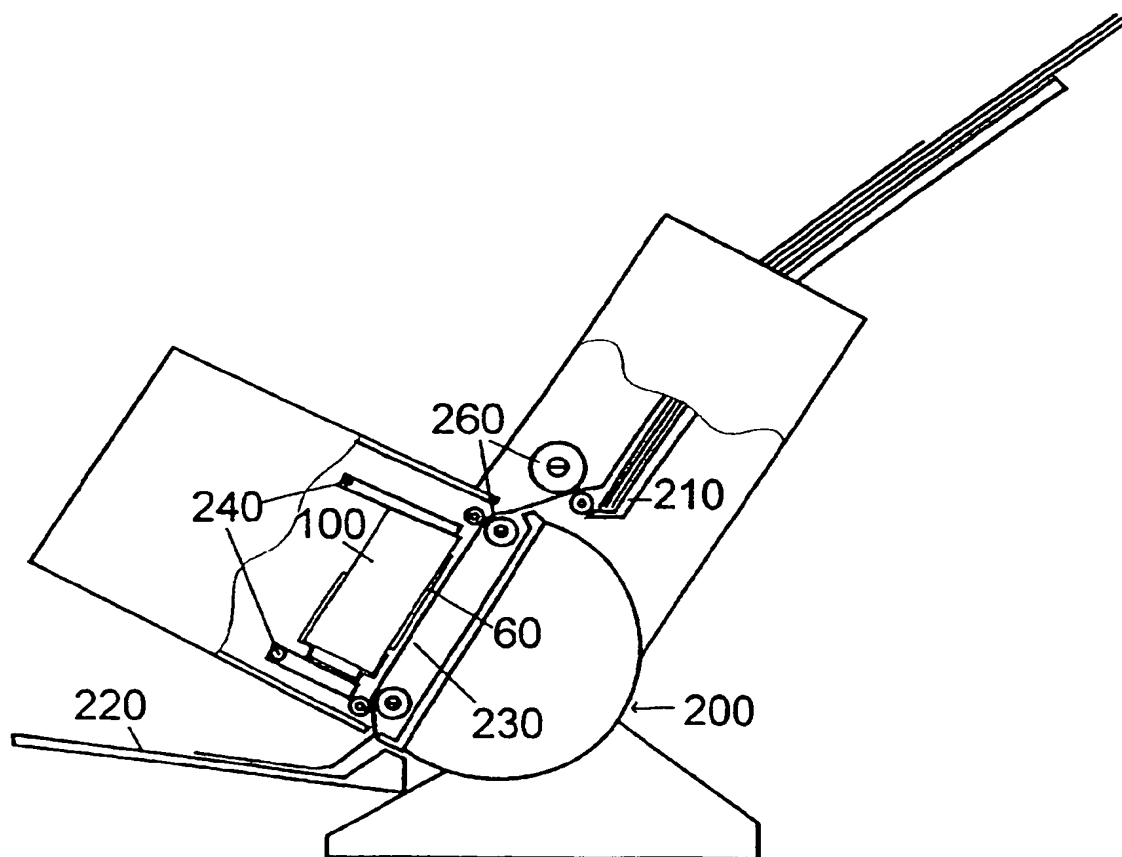
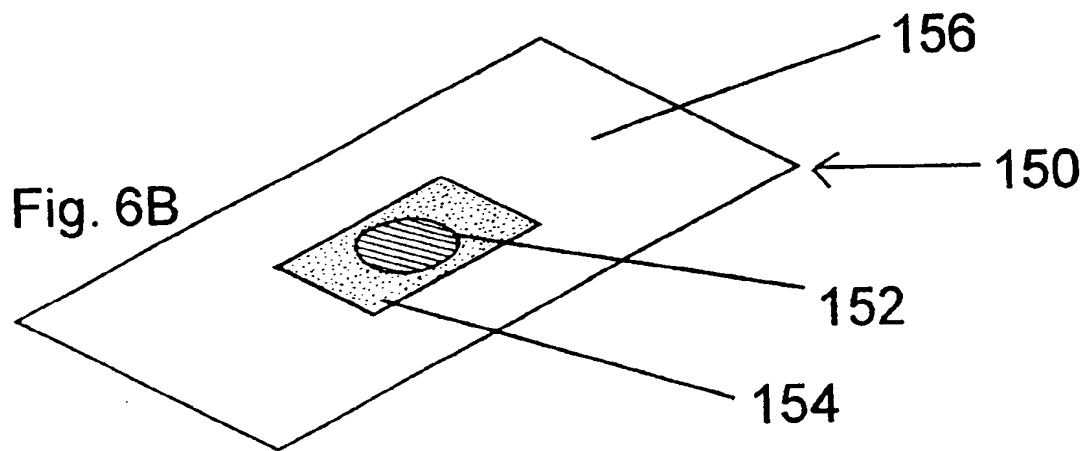
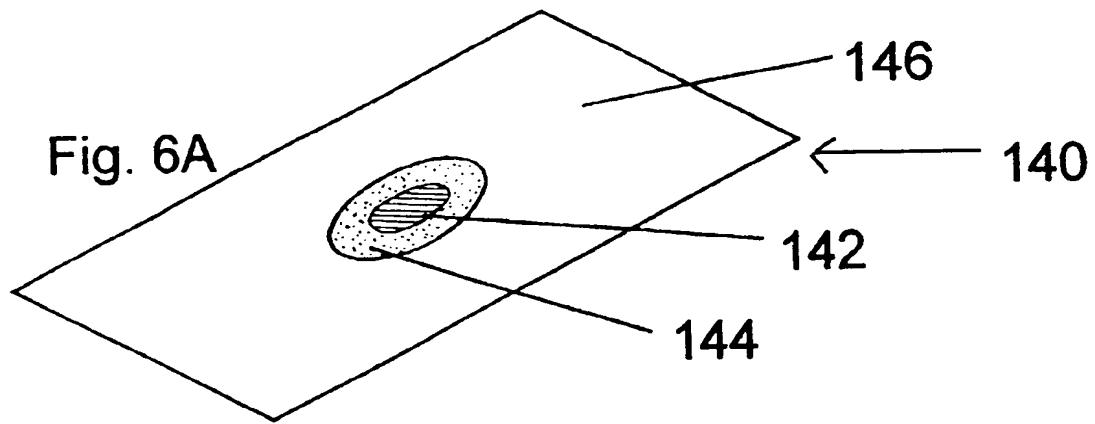


FIG. 5



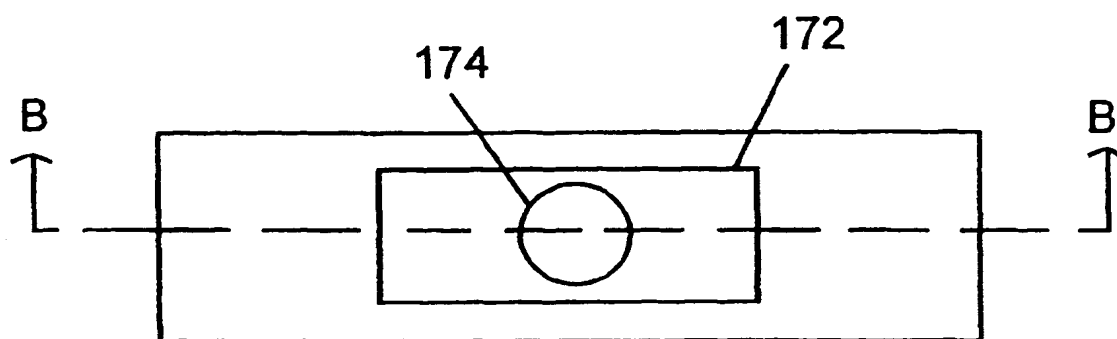


FIG. 7A

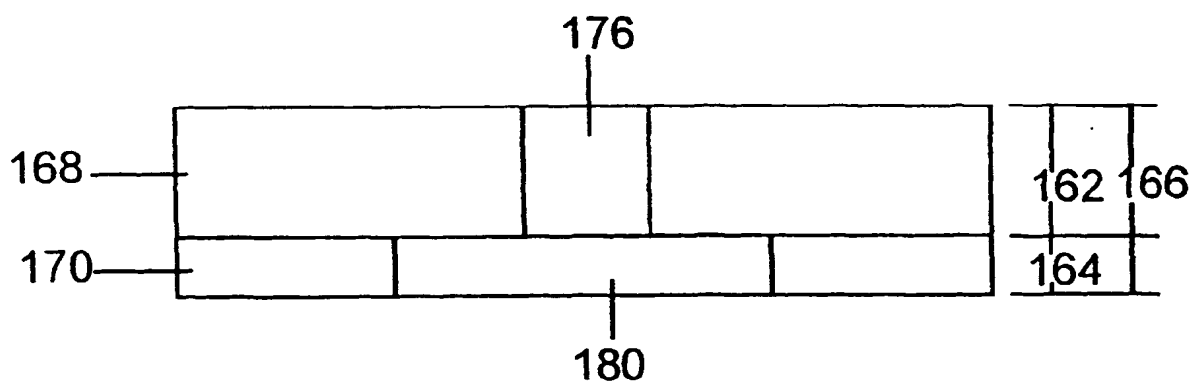


FIG. 7B

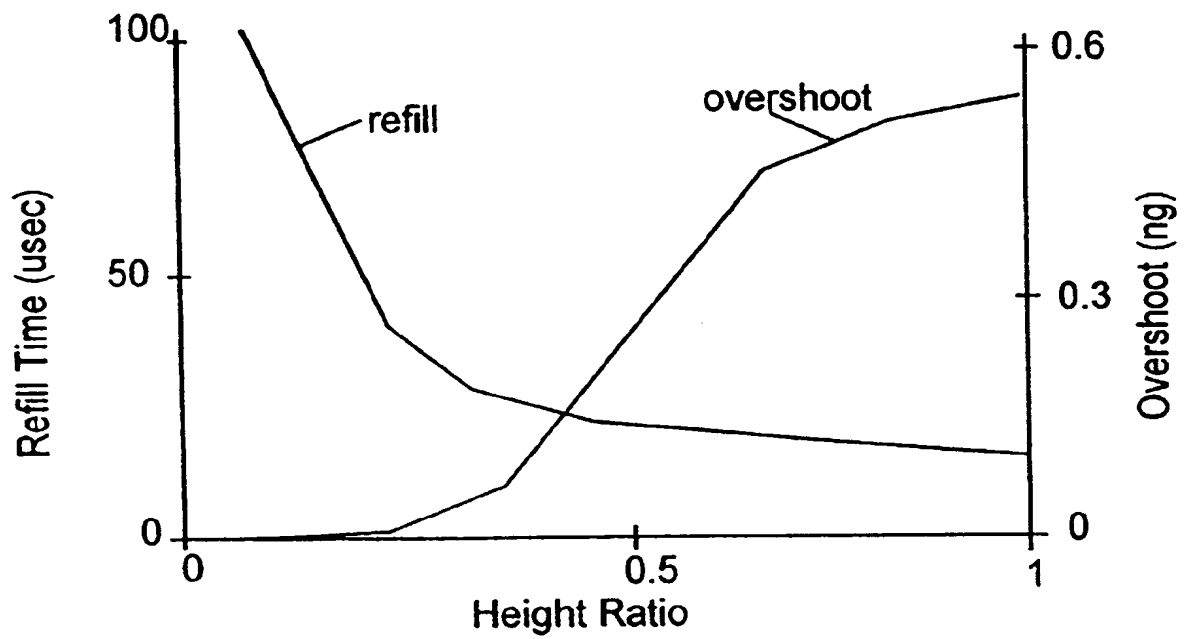


FIG.8

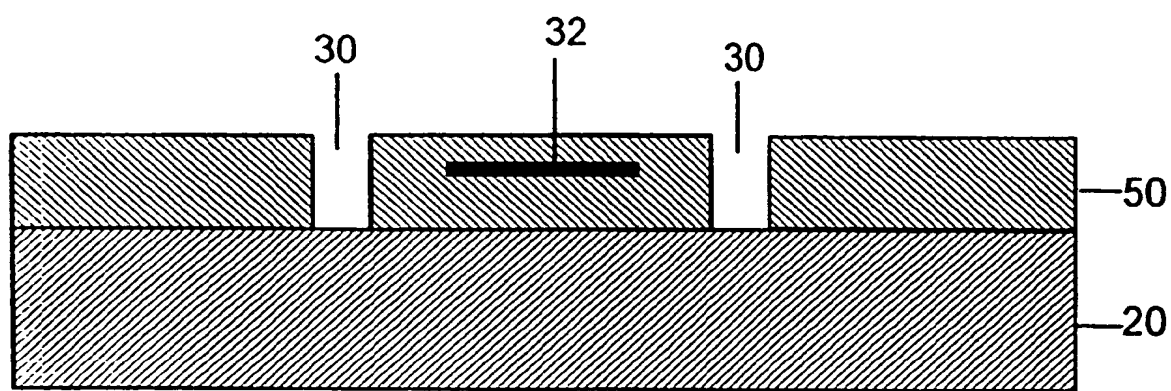


FIG. 9A

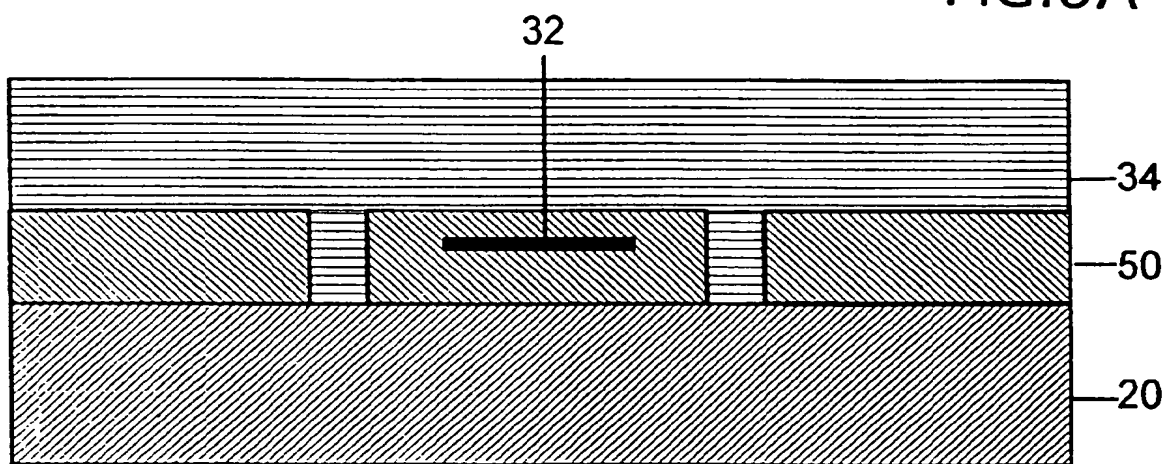


FIG. 9B

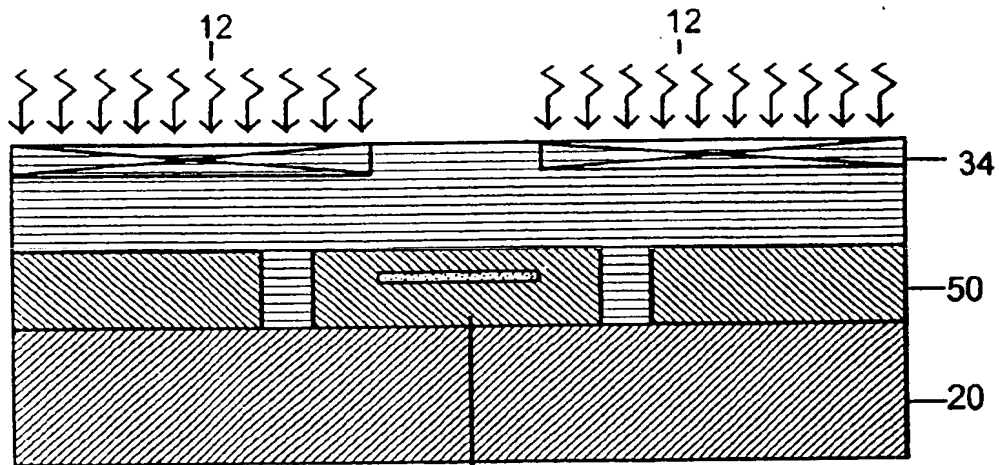


FIG. 9C

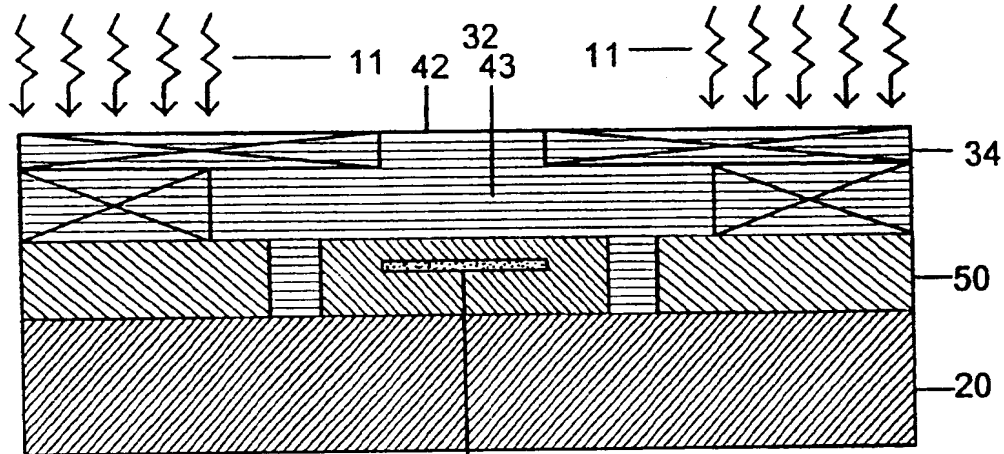


FIG. 9D

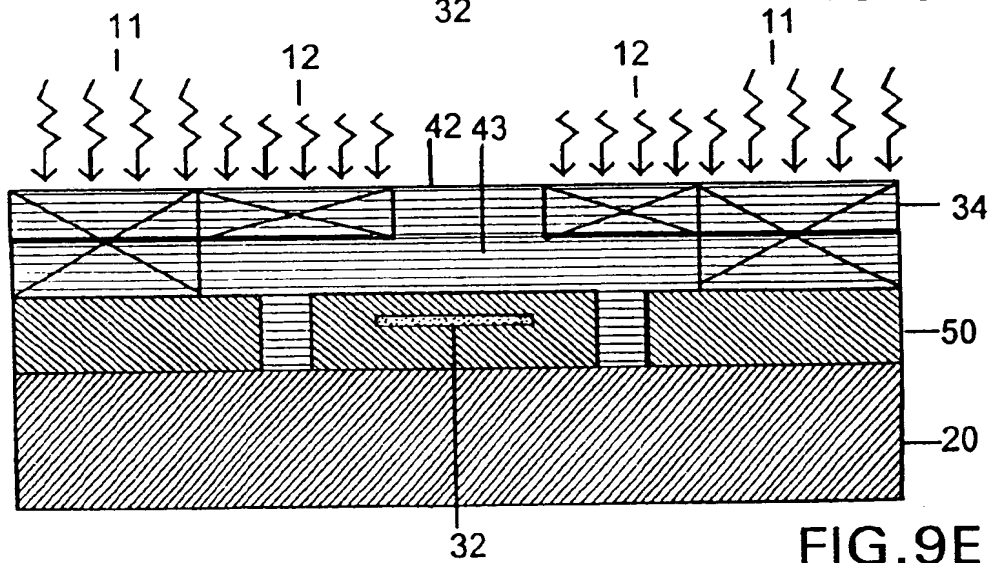


FIG. 9E

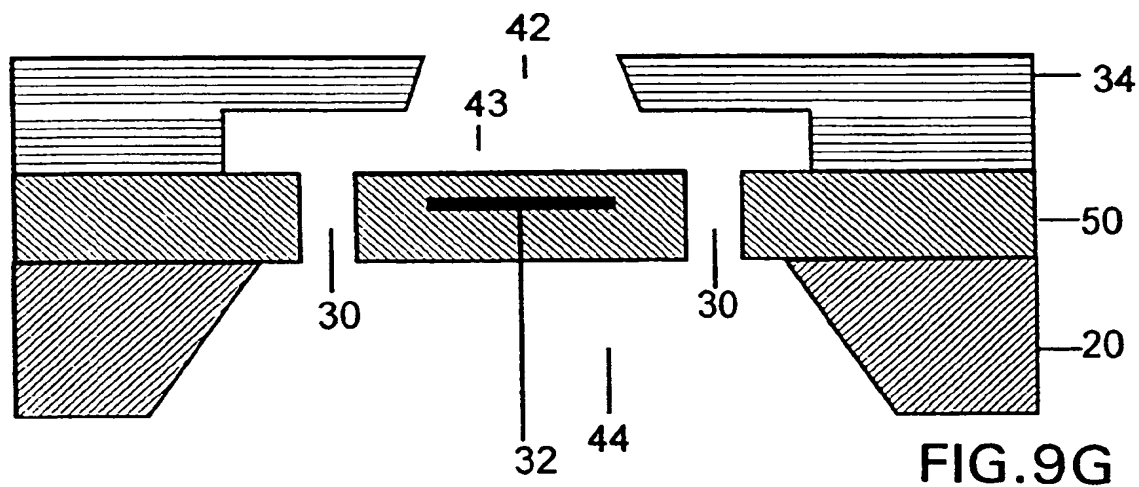
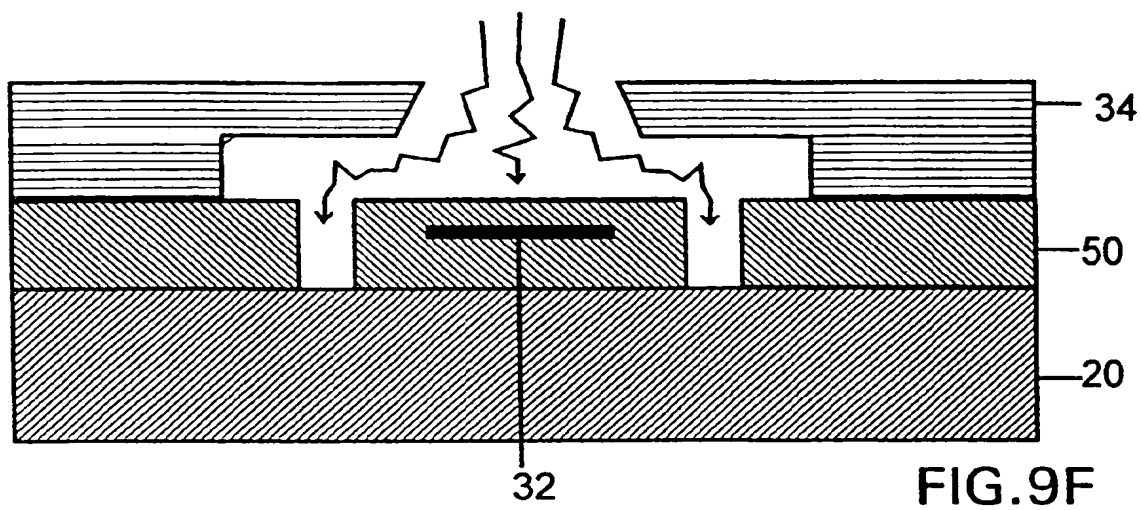




FIG. 10A

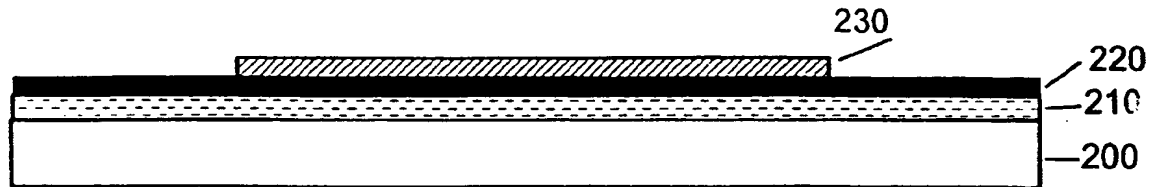


FIG. 10B



FIG. 10C

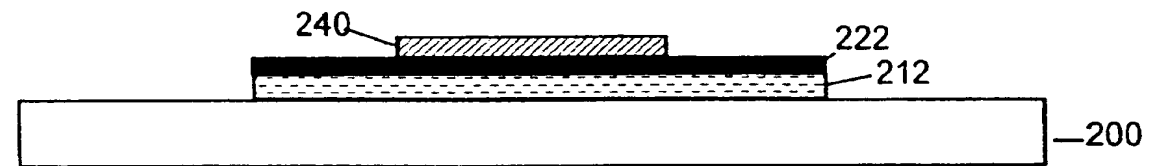


FIG. 10D

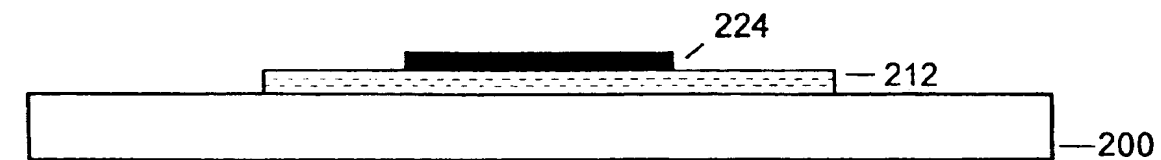


FIG. 10E

(19)



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(11)

EP 0 940 257 A3

(12)

EUROPEAN PATENT APPLICATION

(88) Date of publication A3:
05.04.2000 Bulletin 2000/14

(51) Int Cl. 7: **B41J 2/16, B41J 2/14,**
G03F 7/00

(43) Date of publication A2:
08.09.1999 Bulletin 1999/36

(21) Application number: **99301512.2**

(22) Date of filing: **01.03.1999**

(84) Designated Contracting States:
AT BE CH CY DE DK ES FI FR GB GR IE IT LI LU
MC NL PT SE
Designated Extension States:
AL LT LV MK RO SI

(30) Priority: **02.03.1998 US 33987**

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(54) Direct imaging polymer fluid jet orifice

(57) A process for creating and an apparatus employing shaped orifices in a semiconductor substrate (20). A layer of slow cross-linking material (34) is applied on the semiconductor substrate (20). An orifice image (42) and a fluid-well image (43) is transferred to the layer

of slow cross-linking material (34). That portion of the layer of slow cross-linking material (34) where the orifice image (42) is located is then developed along with that portion of the layer of slow cross-linking material (34) where the fluid well image (43) is located to define an orifice opening in the semiconductor substrate (20).

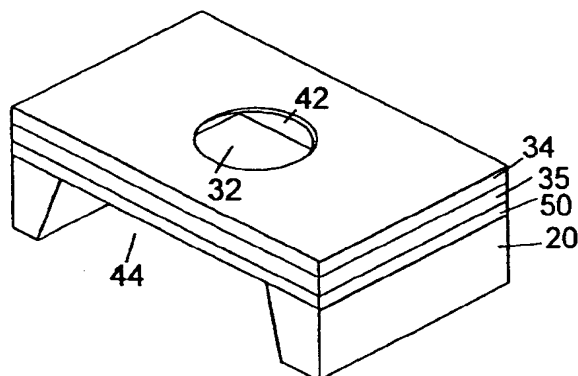


Fig. 1B

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Application Number
EP 99 30 1512

DOCUMENTS CONSIDERED TO BE RELEVANT			
Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int.Cl.6)
Y	US 5 686 224 A (O'NEILL JAMES F) 11 November 1997 (1997-11-11)	1,5-7	B41J2/16 B41J2/14 G03F7/00
A	* abstract; figure 9 * * column 6, line 66 - column 7, line 9 * * column 7, line 60 - column 8, line 13 *	8	
Y	EP 0 244 214 A (HEWLETT PACKARD CO) 4 November 1987 (1987-11-04) * figure 3 *	1,5-7	
A	EP 0 491 560 A (CANON KK) 24 June 1992 (1992-06-24) * page 3, line 28 - page 6, line 58 * * page 8, line 18 - line 20 * * page 8, line 41 - line 49 * * page 9, line 35 - line 51 * * page 15, line 43 - line 45 * * page 16, line 5 - line 12 * * page 10, line 14 - line 16 * * figures *	1-7	TECHNICAL FIELDS SEARCHED (Int.Cl.6) B41J G03F
A	EP 0 734 866 A (CANON KK) 2 October 1996 (1996-10-02) * figures * * page 5, line 25 - line 44 * * page 11, line 20 - line 36 * * page 16, line 48 - line 52 *	1-7	
A	US 4 394 670 A (SUGITANI HIROSHI ET AL) 19 July 1983 (1983-07-19) * column 2, line 8 - line 25 * * figures *	1-7	
Y	US 4 456 371 A (LIN BURN J) 26 June 1984 (1984-06-26) * figure 8C * * abstract *	8,10	
-/--			
The present search report has been drawn up for all claims			
Place of search THE HAGUE		Date of completion of the search 10 February 2000	Examiner Bardet, M
CATEGORY OF CITED DOCUMENTS		T : theory or principle underlying the invention E : earlier patent document, but published on, or after the filing date D : document cited in the application L : document cited for other reasons & : member of the same patent family, corresponding document	
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EUROPEAN SEARCH REPORT

Application Number
EP 99 30 1512

DOCUMENTS CONSIDERED TO BE RELEVANT			
Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int.Cl.6)
Y	US 5 376 483 A (ROLFSON J BRETT) 27 December 1994 (1994-12-27) * column 4, line 58 - line 60 * * column 6, line 48 - line 56 * -----	8, 10	
			TECHNICAL FIELDS SEARCHED (Int.Cl.6)
The present search report has been drawn up for all claims			
Place of search THE HAGUE		Date of completion of the search 10 February 2000	Examiner Bardet, M
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EPPO FORM 1503 03 82 (04/02/01)

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ON EUROPEAN PATENT APPLICATION NO.

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10-02-2000

Patent document cited in search report	Publication date	Patent family member(s)	Publication date
US 5686224 A	11-11-1997	JP 7156409 A	20-06-1995
EP 0244214 A	04-11-1987	EP 0367303 A	09-05-1990
		JP 2716418 B	18-02-1998
		JP 8230192 A	10-09-1996
		JP 2635043 B	30-07-1997
		JP 62259864 A	12-11-1987
		US 4894664 A	16-01-1990
		US 4922265 A	01-05-1990
EP 0491560 A	24-06-1992	JP 2925816 B	28-07-1999
		JP 5124205 A	21-05-1993
		JP 2781466 B	30-07-1998
		JP 4216951 A	07-08-1992
		JP 2694054 B	24-12-1997
		JP 4216952 A	07-08-1992
		JP 4216953 A	07-08-1992
		JP 4216954 A	07-08-1992
		JP 4216955 A	07-08-1992
		JP 4219249 A	10-08-1992
		AT 158754 T	15-10-1997
		DE 69127801 D	06-11-1997
		DE 69127801 T	05-02-1998
		US 5331344 A	19-07-1994
EP 0734866 A	02-10-1996	DE 69603639 D	16-09-1999
		JP 8323985 A	10-12-1996
US 4394670 A	19-07-1983	JP 1601020 C	31-01-1991
		JP 2025335 B	01-06-1990
		JP 57115355 A	17-07-1982
		JP 1814552 C	18-01-1994
		JP 5020274 B	19-03-1993
		JP 57208251 A	21-12-1982
		JP 1497821 C	29-05-1989
		JP 57208252 A	21-12-1982
		JP 63044067 B	02-09-1988
		DE 3200388 A	09-12-1982
		DE 3249980 C	03-11-1994
		GB 2092960 A, B	25-08-1982
US 4456371 A	26-06-1984	EP 0097831 A	11-01-1984
		JP 1041251 B	04-09-1989
		JP 1555915 C	23-04-1990
		JP 59009922 A	19-01-1984

EPO FORM P0459

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**ANNEX TO THE EUROPEAN SEARCH REPORT
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Patent document cited in search report	Publication date	Patent family member(s)	Publication date
US 5376483 A	27-12-1994	NONE	

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